

ABSTRACT OF THE DISCLOSURE

A UART sets a predetermined threshold remaining data amount n , which defines an interrupt position, in a transmission trigger detector before data transmission is completed, checks if a trigger, which indicates the value of a read pointer RP or a count value N has reached a position indicated by the setting, has occurs and, if the trigger occurs, causes a trigger detector to output a interrupt output control signal to an internal interrupt circuit to perform internal interrupt processing. Upon detecting this trigger, the internal interrupt circuit outputs an internal interrupt signal. When all data has not yet been transmitted from a transmission FIFO circuit, a CPU of a host controls the amount of data to be transferred to the transmission FIFO circuit, considering the threshold n , to prevent data in that circuit from being overwritten.

100 99 98 97 96 95 94 93 92 91 90 89 88 87 86 85 84 83 82 81 80 79 78 77 76 75 74 73 72 71 70 69 68 67 66 65 64 63 62 61 60 59 58 57 56 55 54 53 52 51 50 49 48 47 46 45 44 43 42 41 40 39 38 37 36 35 34 33 32 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0